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STATEMENT BY APPLICANT (use as many sheets as necessary)				First Named Inventor	Ana Luisa Lattes
				Art Unit	2819
	(1000			Examiner Name	TRAN, Anh Q.
Sheet	1	of	2	Attorney Docket Number	42P16327

	U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.'	Document Number Number - Kind Code <sup>2</sup> (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passeges or Relevant Figures Appear		
AT		US-6445316	09-03-2002	HSU et al			
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1		XILINX Jan 1995 - FULLY COMPLIANT PCI INTERFACE IN AN XC3164A-2 FPGA	
AI		PARK et al - A HIGH-SPEED MEMORY INTERFACE CIRCUIT TOLERANT TO PVT VARIATIONS AND CHANNEL NOISE	
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